Dr. Howard Chen

Dr. Howard Chen received his B.S. degree from the National Taiwan University in 1979, and Ph.D. degree from the University of California, Berkeley in 1987. Since then, he has been with the IBM Research Division, Thomas J. Watson Research Center, in Yorktown Heights, New York, where he is currently a research staff member. Dr. Chen has been involved in the design and analysis of many microprocessors. He pioneered the research in power supply noise analysis and developed the leading-edge methodology and tools for the design and implementation of IBM eServer products, including S/390 Alliance G4 (Overture), G5 (Symphony), G6 (Opera), G7 (Freeway), AS/400 Pulsar, N-Star, I-Star, S-Star, PowerPC 604 Helmwind, Lonestar, Longhorn, Glacier, pSeries GP (Power4 Regatta), GR, zSeries T-Rex, T-Saurs, GPUL (Apple Power Mac G5), Gekko (Nintendo GameCube), Waternoose, Loki, Vejle (Microsoft Xbox), and STI (Sony, Toshiba, IBM) cell processors.

Dr. Chen has received the IBM Invention Achievement Awards for 23 U.S. patents issued, the IBM Research Division Award for contributions to the design and realization of the Alliance G4 microprocessor, the IBM Outstanding Contribution Award for design and realization of the Alliance G5 microprocessor, the IBM Research Division Award for design and implementation of Freeway G7 microprocessor, and the IBM Outstanding Technical Achievement Award for NOVA-ALSIM-CPAM-IREM multi-site multi-division awards package. He has also given tutorials and workshops at the Design Automation Conference (DAC), Asia and South Pacific Design Automation Conference (ASP-DAC), the European Solid-State Circuits Conference (ESSIRC), and the International Solid-State Circuits Conference (ISSCC). Dr. Chen has published over 60 technical papers and is currently a senior member of IEEE.